There are two ways to think about your ISR for Lab 5.
- Interrupt handler.
- Event handler.
- Correct lab 4 code will be provided to aid in lab 5.

- Embedded systems design metrics.
- Top-down/bottom-up design process.
- 6812 architecture.
- Binary arithmetic.
- Assembly language.
- Software design & engineering.
- Finite state machines.
- Gadfly & blind cycle synchronization.
- Debouncing & keypads.

- Data interpretation.
- Finite state machine creation.
- Button/keypad interfacing.
  - Debouncing keys.
  - Pull-up/pull-down resistors.