CS/ECE 3700-- HW Assignment #4
Due Wednesday February 8th by 5:00pm
Turn in to the 3700 slot outside the SoC office
Remember to put BOTH your name and lab section number on the front!

This assignment is a set of problems from the book that will give you practice with transistor circuits and PLAs

Problems from the book: 3.1, 3.2, 3.6, 3.10, 3.37, 3.44,
3.51 (in Verilog, write code only),
3.53 (in Verilog, write code only).

Additional Problem:

Based on the PLA in Figure 3.27 in your book, draw a PLA template with an implementation of both of the following functions in the same PLA:

\[ f_1(A, B, C) = \sum m(0,3,4) \]
\[ f_2(A, B, C) = \sum m(0,1,4,5) \]